



#3

Docket No. 210067US2/sm

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

1 DS
10/31/01
D Bell

IN RE APPLICATION OF: Hidemasa ZAMA, et al.

SERIAL NO: 09/883,959

GAU: 2814

FILED: June 20, 2001

EXAMINER:

FOR: SEMICONDUCTOR INTEGRATED CIRCUIT, LOGIC OPERATION CIRCUIT, AND FLIP FLOP

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97

ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

SIR:

Applicant(s) wish to disclose the following information.

REFERENCES

- The applicant(s) wish to make of record the references listed on the attached form PTO-1449. Copies of the listed references are attached, where required, as are either statements of relevancy or any readily available English translations of pertinent portions of any non-English language references.
- A check is attached in the amount required under 37 CFR §1.17(p).

RELATED CASES

- Attached is a list of applicant's pending application(s) or issued patent(s) which may be related to the present application. A copy of the patent(s), together a copy of the claims and drawings of the pending application(s) is attached along with PTO 1449.
- A check is attached in the amount required under 37 CFR §1.17(p).

CERTIFICATION

- Each item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.
- No item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned, having made reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this statement.

DEPOSIT ACCOUNT

- Please charge any additional fees for the papers being filed herewith and for which no check is enclosed herewith, or credit any overpayment to deposit account number 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.

Marvin J. Spivak
Registration No. 24,913

Surinder Sachar
Registration No. 34,423

TECHNOLOGY CENTER 2800

AUG 23 2001

RECEIVED



22850

Tel. (703) 413-3000
Fax. (703) 413-2220
(OSMMN 10/98)

DOCKET NO: 210067US
SERIAL NO: 09/883,959



Sheet 1 of 1

Group Art Unit: 2814

STATEMENT OF RELEVANCY

Reference AW on Form 1449:

This reference shows the already known MTCMOS circuits.

RECEIVED
AUG. 23 2001
TECHNOLOGY CENTER 2800